Initial Prep

Initials _______ Date ___/___/___

☐ Serial Number Sticker and Marker
☐ Stiffener Bar
☐ Trim stubs
☐ Standoffs
☐ Jumpers
☐ Fuses
☐ Set VME address

Daughter Cards

Initials _______ Date ___/___/___ PB Serial Number ________

☐ Attach tail
☐ Install and cable TX and RX
☐ Install Physics Board
  a. Serial Number Sticker and Marker
  b. Clip off test points

JTAG Programming

Initials _______ Date ___/___/___

☐ “Genesis” connector - jtagboot.jcf
☐ Front panel connector - tpatchips.jcf
☐ Remove jumper from J3
☐ Front panel connector - mcenball.jcf
**Testing**

Initials _______  Date ___/___/___

- Run loop test (>= 10k loops) with zero errors
- Blue dot
- Install nylon nuts

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Notes (initial and date each)