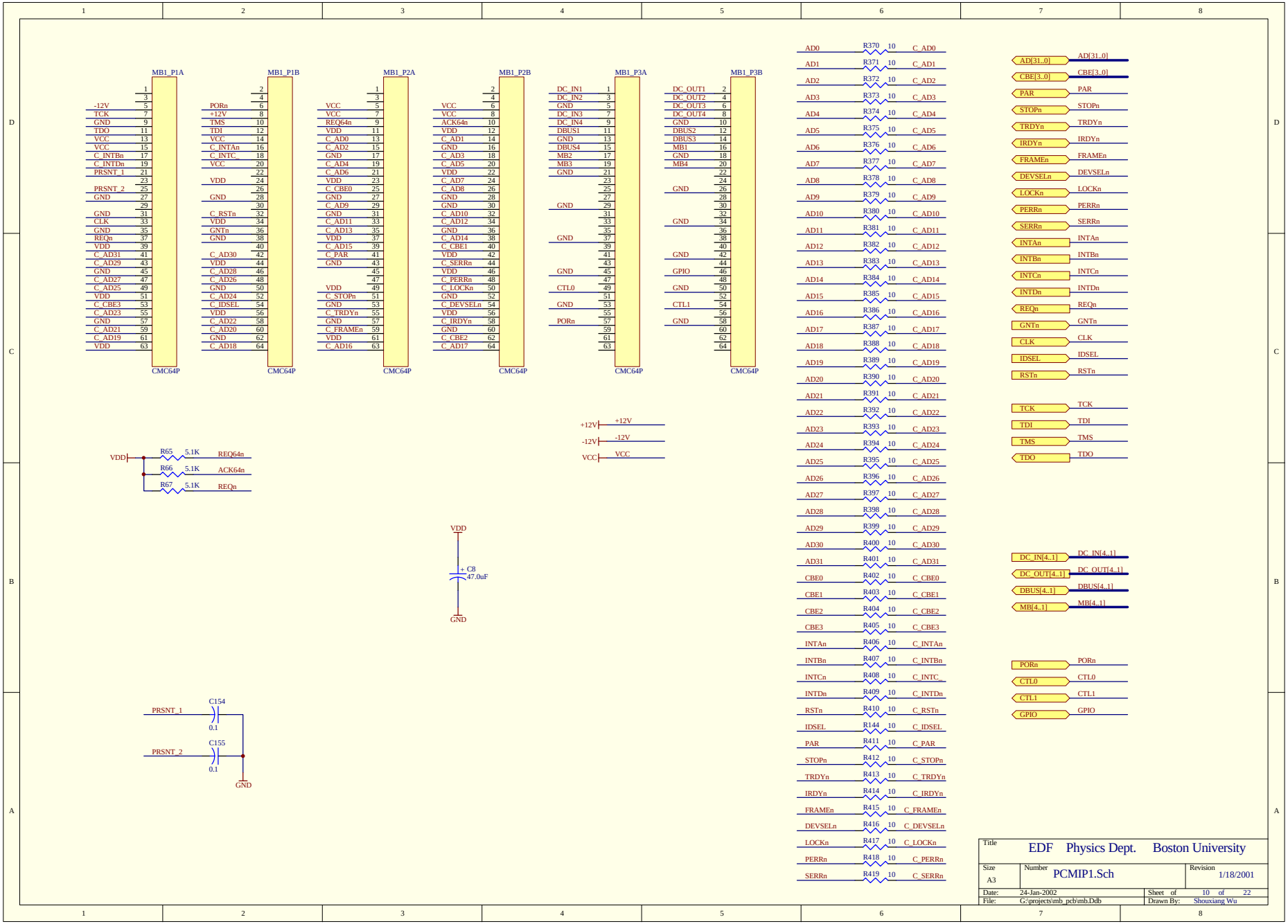
MB1_P1A
CMC64P
1 -12V
3 TCK
5 GND
7 TDO
9 VCC
11 VCC
13 C_INTBn
15 C_INTDn
17 PRSNT_1
19 PRSNT_2
21 GND
23 CLK
25 GND
27 REQn
29 VDD
31 C_AD31
33 C_AD29
35 GND
37 C_AD27
39 C_AD25
41 VDD
43 C_CBE3
45 C_AD23
47 GND
49 C_AD21
51 C_AD19
53 VDD
MB1_P1B
CMC64P
2 PORn
4 +12V
6 TMS
8 TDI
10 VCC
12 C_INTAn
14 C_INTC_
16 VCC
18 VDD
20 GND
22 C_RSTn
24 VDD
26 GNTn
28 GND
30 C_AD30
32 VDD
34 C_AD28
36 C_AD26
38 GND
40 C_AD24
42 C_IDSEL
44 VDD
46 C_AD22
48 C_AD20
50 GND
52 C_AD18
MB1_P2A
CMC64P
1 VCC
3 VCC
5 REQ64n
7 VDD
9 C_AD0
11 C_AD2
13 GND
15 C_AD4
17 C_AD6
19 VDD
21 C_CBE0
23 GND
25 C_AD9
27 GND
29 C_AD11
31 C_AD13
33 VDD
35 C_AD15
37 C_PAR
39 GND
41 VDD
43 C_STOPn
45 GND
47 C_TRDYn
49 GND
51 C_FRAMEn
53 VDD
55 C_AD16
57
MB1_P2B
CMC64P
2 VCC
4 VCC
6 ACK64n
8 VDD
10 C_AD1
12 GND
14 C_AD3
16 C_AD5
18 VDD
20 C_AD7
22 C_AD8
24 GND
26 GND
28 C_AD10
30 C_AD12
32 GND
34 C_AD14
36 C_CBE1
38 VDD
40 C_SERRn
42 VDD
44 C_PERRn
46 C_LOCKn
48 GND
50 C_DEVSELn
52 VDD
54 C_IRDYn
56 GND
58 C_CBE2
60 C_AD17
62
64
MB1_P3A
CMC64P
1 DC_IN1
3 DC_IN2
5 GND
7 DC_IN3
9 DC_IN4
11 DBUS1
13 GND
15 DBUS4
17 MB2
19 MB3
21 GND
23
25
27
29 GND
31
33
35
37 GND
39
41
43
45 GND
47
49 CTL0
51
53 GND
55
57 PORn
59
61
63
MB1_P3B
CMC64P
2 DC_OUT1
4 DC_OUT2
6 DC_OUT3
8 DC_OUT4
10 GND
12 DBUS2
14 DBUS3
16 MB1
18 GND
20 MB4
22
24
26 GND
28
30
32
34 GND
36
38
40
42 GND
44
46 GPIO
48
50 GND
52
54 CTL1
56
58 GND
60
62
64
VDD
R65 5.1K REQ64n
R66 5.1K ACK64n
R67 5.1K REQn
+12V
-12V
VCC
VDD
+ C8
47.0uF
GND
PRSNT_1
C154
0.1
PRSNT_2
C155
0.1
GND
AD0 R370 10 C_AD0
AD1 R371 10 C_AD1
AD2 R372 10 C_AD2
AD3 R373 10 C_AD3
AD4 R374 10 C_AD4
AD5 R375 10 C_AD5
AD6 R376 10 C_AD6
AD7 R377 10 C_AD7
AD8 R378 10 C_AD8
AD9 R379 10 C_AD9
AD10 R380 10 C_AD10
AD11 R381 10 C_AD11
AD12 R382 10 C_AD12
AD13 R383 10 C_AD13
AD14 R384 10 C_AD14
AD15 R385 10 C_AD15
AD16 R386 10 C_AD16
AD17 R387 10 C_AD17
AD18 R388 10 C_AD18
AD19 R389 10 C_AD19
AD20 R390 10 C_AD20
AD21 R391 10 C_AD21
AD22 R392 10 C_AD22
AD23 R393 10 C_AD23
AD24 R394 10 C_AD24
AD25 R395 10 C_AD25
AD26 R396 10 C_AD26
AD27 R397 10 C_AD27
AD28 R398 10 C_AD28
AD29 R399 10 C_AD29
AD30 R400 10 C_AD30
AD31 R401 10 C_AD31
CBE0 R402 10 C_CBE0
CBE1 R403 10 C_CBE1
CBE2 R404 10 C_CBE2
CBE3 R405 10 C_CBE3
INTAn R406 10 C_INTAn
INTBn R407 10 C_INTBn
INTCn R408 10 C_INTC_
INTDn R409 10 C_INTDn
RSTn R410 10 C_RSTn
IDSEL R144 10 C_IDSEL
PAR R411 10 C_PAR
STOPn R412 10 C_STOPn
TRDYn R413 10 C_TRDYn
IRDYn R414 10 C_IRDYn
FRAMEn R415 10 C_FRAMEn
DEVSELn R416 10 C_DEVSELn
LOCKn R417 10 C_LOCKn
PERRn R418 10 C_PERRn
SERRn R419 10 C_SERRn
AD[31..0] AD[31..0]
CBE[3..0] CBE[3..0]
PAR PAR
STOPn STOPn
TRDYn TRDYn
IRDYn IRDYn
FRAMEn FRAMEn
DEVSELn DEVSELn
LOCKn LOCKn
PERRn PERRn
SERRn SERRn
INTAn INTAn
INTBn INTBn
INTCn INTCn
INTDn INTDn
REQn REQn
GNTn GNTn
CLK CLK
IDSEL IDSEL
RSTn RSTn
TCK TCK
TDI TDI
TMS TMS
TDO TDO
DC_IN[4..1] DC_IN[4..1]
DC_OUT[4..1] DC_OUT[4..1]
DBUS[4..1] DBUS[4..1]
MB[4..1] MB[4..1]
PORn PORn
CTL0 CTL0
CTL1 CTL1
GPIO GPIO
Title EDF Physics Dept. Boston University
Size A3
Number PCMIP1.Sch
Revision 1/18/2001
Date: 24-Jan-2002
Sheet of 10 of 22
File: G:\projects\mb_pcb\mb.Ddb
Drawn By: Shouxiang Wu