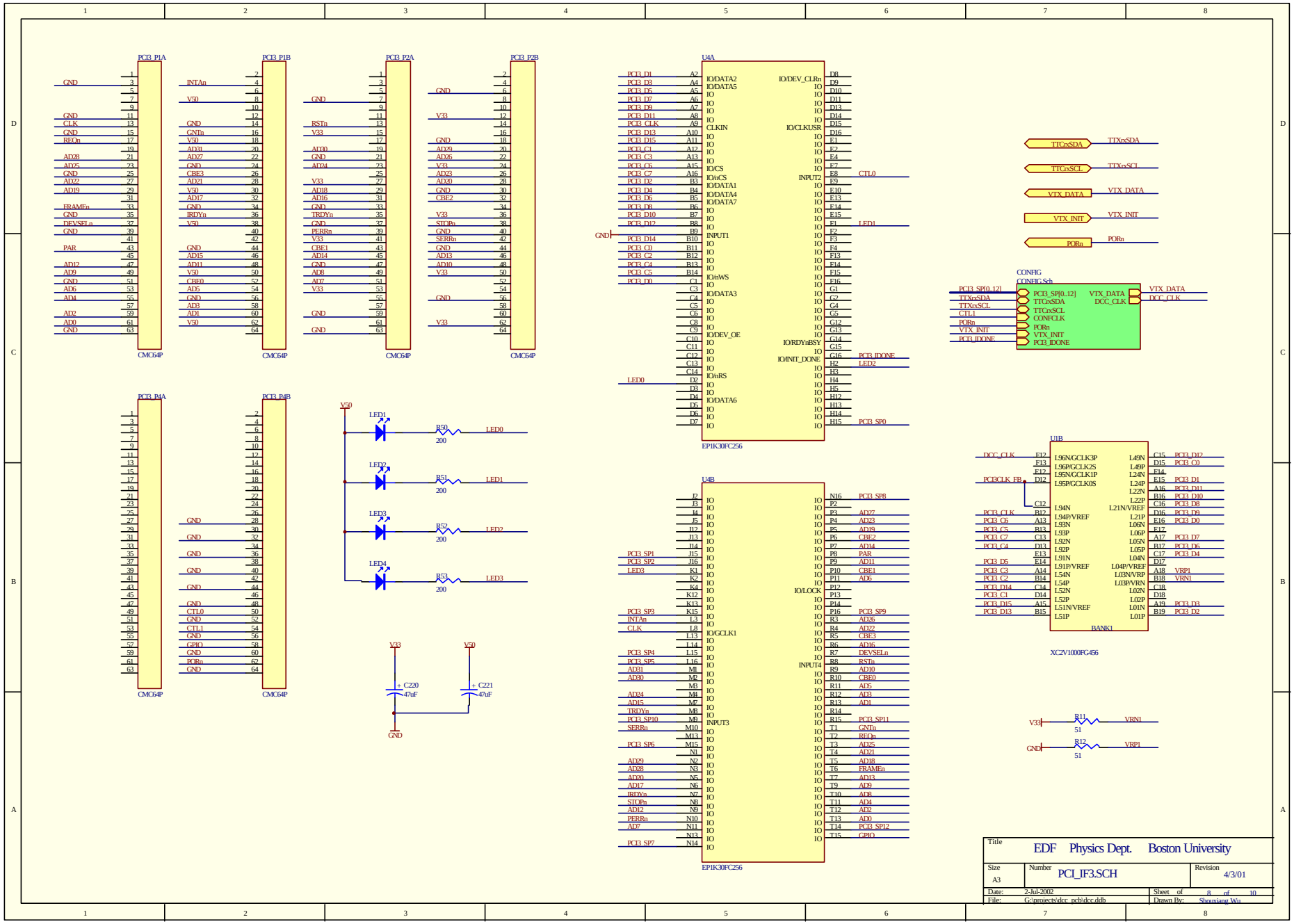
PCI3_P1A
PCI3_P1B
PCI3_P2A
PCI3_P2B
PCI3_P4A
PCI3_P4B
CMC64P
U4A
EP1K30FC256
U4B
EP1K30FC256
U1B
BANK1
XC2V1000FG456
CONFIG
CONFIG.Sch
LED0
LED1
LED2
LED3
LED4
R50
R51
R52
R53
200
V50
V33
C220
C221
47uF
GND
R11
R12
51
VRN1
VRP1
TTCrxSDA
TTCrxSCL
VTX_DATA
VTX_INIT
PORn
DCC_CLK
CTL0
PCI3_IDONE
PCI3_SP0
PCI3_SP8
PCI3_SP11
PCI3_SP12
PCI3_SP9
PCI3_SP10
PCI3_SP[0..12]
CONFCLK
PCI3_CLK
PCI3CLK_FB
Title
EDF Physics Dept. Boston University
Size
A3
Number
PCI_IF3.SCH
Revision
4/3/01
Date: 2-Jul-2002
Sheet 8 of 10
File: G:\projects\dcc_pcb\dcc.ddb
Drawn By: Shouxiang Wu