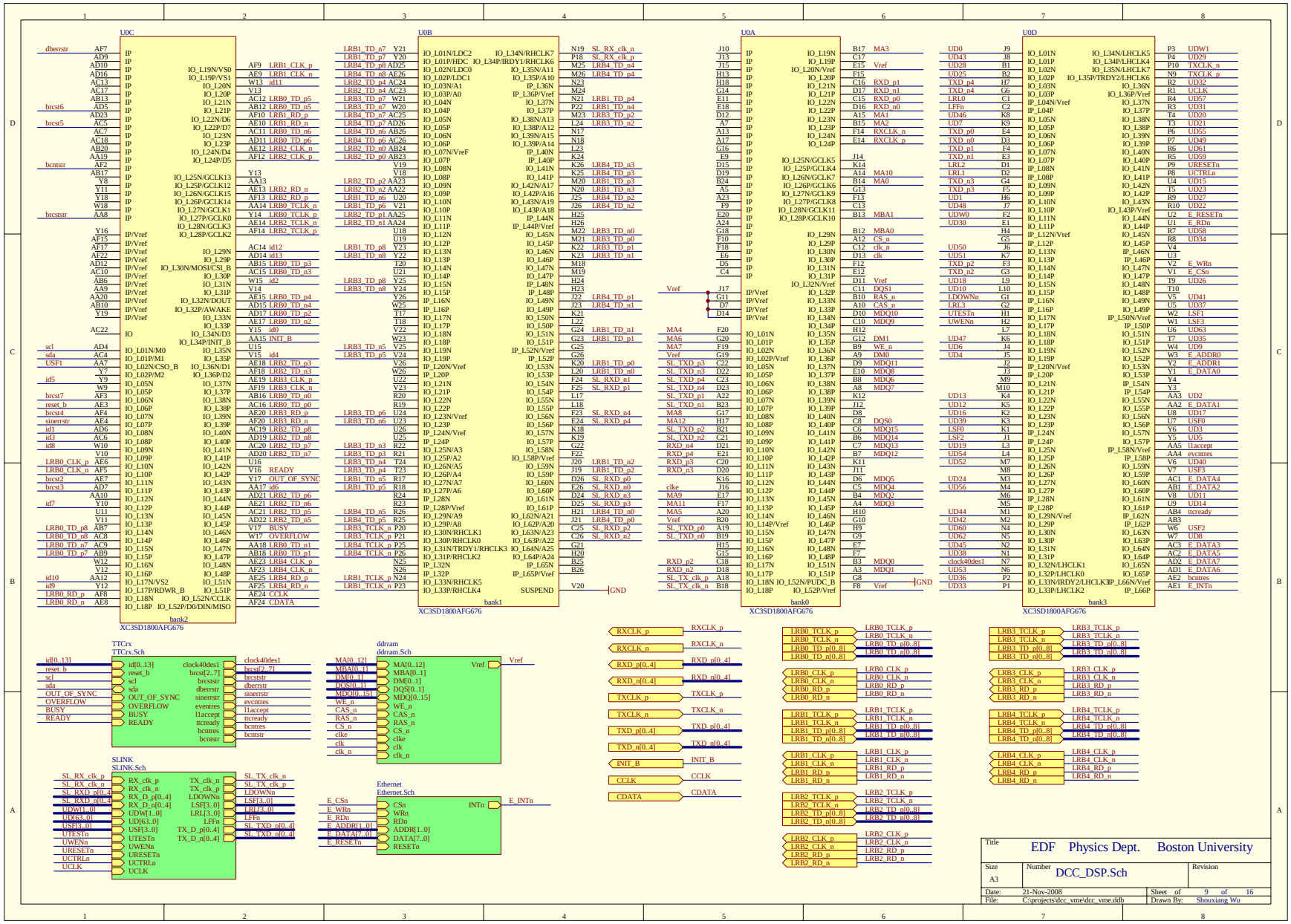
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bank2
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U0A
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bank0
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XC3SD1800AFG676
bank3
TTCrx
TTCrx.Sch
ddrram
ddrram.Sch
SLINK
SLINK.Sch
Ethernet
Ethernet.Sch
EDF Physics Dept. Boston University
Title
Size
A3
Number
DCC_DSP.Sch
Revision
Date: 21-Nov-2008
Sheet of 9 of 16
File: C:\projects\dcc_vme\dcc_vme.ddb
Drawn By: Shouxiang Wu