Receiving:
- 26.5mhz clock
  - The beam clock is 53mhz, but we are getting a processed clock, which is half the frequency
- Trigger signal
  - This is a patterned signal as 1,0,1 each as one 26.5mhz clock cycle

Required computations:
- Count triggers starting at 0 for each spill (20 bit counter)
  - Regardless of the shutter
- Count time between triggers in units of 26.5mhz clock cycles
  - Or count time of triggers WRT shutter opening in units of 26.5mhz clock cycles

Possible computations:
- Record trigger times with both the 40mhz and 26.5mhz clock
- Count triggers between shutter openings
- Request end of spill signal and record the time
- More…