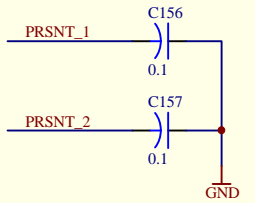
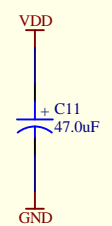
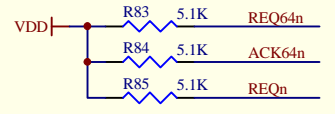
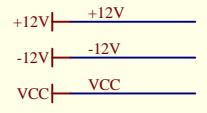


MB6 P1A		MB6 P1B	
1	-12V	2	PORn
3	TCK	4	+12V
5	GND	6	TMS
7	VCC	8	TDI
9	VCC	10	VCC
11	C_INTBn	12	C_INTAn
13	C_INTDn	14	C_INTC
15	PRSN1_1	16	VCC
17	PRSN1_2	18	VDD
19	GND	20	VDD
21	GND	22	C_CBE0
23	GND	23	GND
25	GND	24	C_AD9
27	GND	25	GND
29	GND	26	C_AD10
31	GND	27	C_AD11
33	CLK	28	C_AD12
35	GND	29	C_AD13
37	REQn	30	VDD
39	VDD	31	C_AD14
41	C_AD31	32	C_CBE1
43	C_AD29	33	VDD
45	GND	34	C_SERRn
47	C_AD27	35	VDD
49	C_AD25	36	C_PERRn
51	VDD	37	GND
53	C_CBE3	38	C_AD15
55	C_AD23	39	C_PAR
57	GND	40	VDD
59	C_AD21	41	C_TRDYn
61	C_AD19	42	GND
63	VDD	43	C_AD24
		44	C_IDSEL
		45	VDD
		46	C_AD22
		47	C_AD26
		48	GND
		49	C_AD25
		50	VDD
		51	C_AD24
		52	C_IDSEL
		53	GND
		54	C_TRDYn
		55	VDD
		56	C_AD22
		57	C_AD26
		58	GND
		59	C_AD25
		60	VDD
		61	C_AD24
		62	C_IDSEL
		63	GND
		64	C_TRDYn

MB6 P2A		MB6 P2B	
1	VCC	1	VCC
3	VCC	2	VCC
5	REQ64n	3	ACK64n
7	VDD	4	VDD
9	C_AD0	5	C_AD1
11	GND	6	GND
13	C_AD2	7	C_AD3
15	GND	8	C_AD5
17	C_AD4	9	VDD
19	C_AD6	10	C_AD7
21	VDD	11	C_AD8
23	VDD	12	GND
25	C_CBE0	13	C_AD9
27	GND	14	C_AD10
29	C_AD9	15	C_AD11
31	GND	16	C_AD12
33	C_AD11	17	GND
35	C_AD13	18	C_AD14
37	VDD	19	C_CBE1
39	C_AD15	20	VDD
41	C_PAR	21	C_SERRn
43	GND	22	VDD
45	VDD	23	C_PERRn
47	C_STOPn	24	GND
49	GND	25	C_DEVSELn
51	C_STOPn	26	VDD
53	GND	27	C_IRDYn
55	C_TRDYn	28	GND
57	GND	29	C_LOCKn
59	C_FRAMEEn	30	GND
61	VDD	31	C_DEVSELn
63	C_AD16	32	VDD

MB6 P3A		MB6 P3B	
1	DC_IN1	1	DC_OUT1
3	DC_IN2	2	DC_OUT2
5	GND	3	DC_OUT3
7	DC_IN3	4	DC_OUT4
9	DC_IN4	5	GND
11	DBUS1	6	DBUS2
13	GND	7	DBUS3
15	DBUS4	8	MB1
17	MB2	9	GND
19	MB3	10	MB4
21	GND	11	GND
23	GND	12	GND
25	GND	13	GND
27	GND	14	GND
29	GND	15	GND
31	GND	16	GND
33	GND	17	GND
35	GND	18	GND
37	GND	19	GND
39	GND	20	GND
41	GND	21	GND
43	GND	22	GND
45	GND	23	GND
47	GND	24	GND
49	CTL0	25	GND
51	GND	26	GND
53	GND	27	GND
55	GND	28	GND
57	PORn	29	GND
59	GND	30	GND
61	GND	31	GND
63	GND	32	GND



AD0	R470	10	C_AD0
AD1	R471	10	C_AD1
AD2	R472	10	C_AD2
AD3	R473	10	C_AD3
AD4	R474	10	C_AD4
AD5	R475	10	C_AD5
AD6	R476	10	C_AD6
AD7	R477	10	C_AD7
AD8	R478	10	C_AD8
AD9	R479	10	C_AD9
AD10	R480	10	C_AD10
AD11	R481	10	C_AD11
AD12	R482	10	C_AD12
AD13	R483	10	C_AD13
AD14	R484	10	C_AD14
AD15	R485	10	C_AD15
AD16	R486	10	C_AD16
AD17	R487	10	C_AD17
AD18	R488	10	C_AD18
AD19	R489	10	C_AD19
AD20	R490	10	C_AD20
AD21	R491	10	C_AD21
AD22	R492	10	C_AD22
AD23	R493	10	C_AD23
AD24	R494	10	C_AD24
AD25	R495	10	C_AD25
AD26	R496	10	C_AD26
AD27	R497	10	C_AD27
AD28	R498	10	C_AD28
AD29	R499	10	C_AD29
AD30	R500	10	C_AD30
AD31	R501	10	C_AD31
CBE0	R502	10	C_CBE0
CBE1	R503	10	C_CBE1
CBE2	R504	10	C_CBE2
CBE3	R505	10	C_CBE3
INTAn	R506	10	C_INTAn
INTBn	R507	10	C_INTBn
INTCn	R508	10	C_INTCn
INTDn	R509	10	C_INTDn
RSTn	R510	10	C_RSTn
IDSEL	R147	1K	C_IDSEL
PAR	R511	10	C_PAR
STOPn	R512	10	C_STOPn
TRDYn	R513	10	C_TRDYn
IRDYn	R514	10	C_IRDYn
FRAMEEn	R515	10	C_FRAMEEn
DEVSELn	R516	10	C_DEVSELn
LOCKn	R517	10	C_LOCKn
PERRn	R518	10	C_PERRn
SERRn	R519	10	C_SERRn

AD[31..0]	AD[31..0]
CBE[3..0]	CBE[3..0]
PAR	PAR
STOPn	STOPn
TRDYn	TRDYn
IRDYn	IRDYn
FRAMEEn	FRAMEEn
DEVSELn	DEVSELn
LOCKn	LOCKn
PERRn	PERRn
SERRn	SERRn
INTAn	INTAn
INTBn	INTBn
INTCn	INTCn
INTDn	INTDn
REQn	REQn
GNTn	GNTn
CLK	CLK
IDSEL	IDSEL
RSTn	RSTn

TCK	TCK
TDI	TDI
TMS	TMS
TDO	TDO

DC_IN[4..1]	DC_IN[4..1]
DC_OUT[4..1]	DC_OUT[4..1]
DBUS[4..1]	DBUS[4..1]
MB[4..1]	MB[4..1]

PORn	PORn
CTL0	CTL0
CTL1	CTL1
GPIO	GPIO

Title			EDF Physics Dept. Boston University		
Size	Number	Revision			
A3	PCMP6.Sch	8/4/2000			
Date:	27-Sep-2000	Sheet of	13 of 22		
File:	E:\STT_PCB\mb.Ddb	Drawn By:	Shouxiang Wu		