

A

B

C

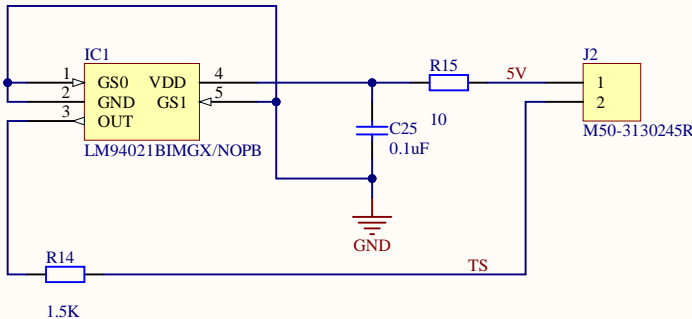
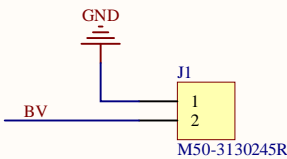
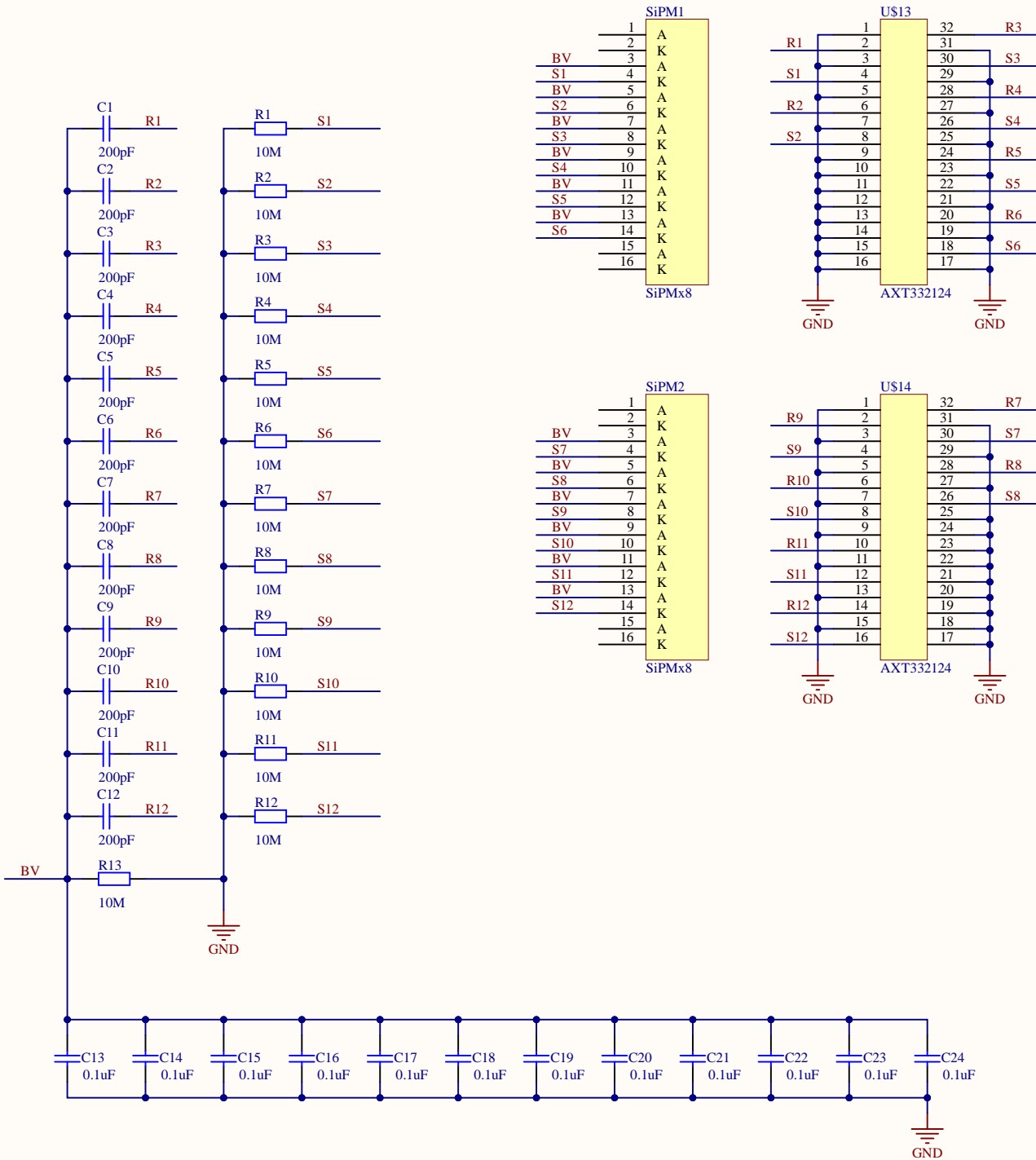
D

A

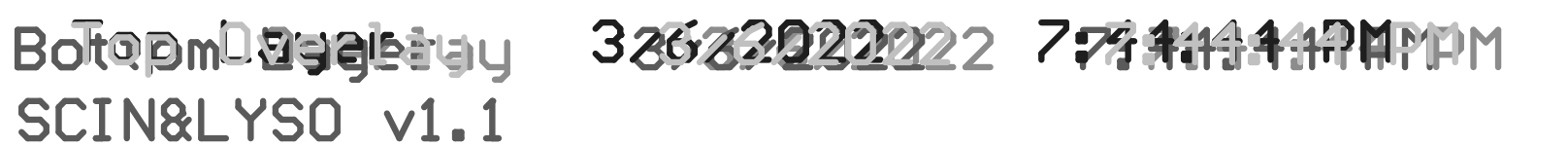
B

C














D



Title		
LYSO_T		
Size	Number	Revision
B		v1.1
Date:	3/06/2022	Sheet bf 1
File:	D:\projects\LYSO_T\LYSO_T.SchDoc	Drawn By:



Board Stack Report

Stack Up		Layer Stack			
Layer	Board Layer Stack	Name	Material	Thickness	Constant
1		Top Paste			
2		Top Overlay			
3		Top Solder	Solder Resist	0.010mm	3.5
4		Top Layer	Copper	0.036mm	
5		Dielectric 1	PP-006	0.150mm	4.1
6		plane1	Copper	0.035mm	
7		Dielectric 2	FR-4	1.500mm	4.8
8		plane2	Copper	0.035mm	
9		Dielectric 3	PP-006	0.150mm	4.1
10		Bottom Layer	Copper	0.036mm	
11		Bottom Solder	Solder Resist	0.010mm	3.5
12		Bottom Overlay			
13		Bottom Paste			
	Height : 1.961mm				

Comment	Description	Designator	Footprint	LibRef	Quantity
GRM1885C2A201JA01 D	Capacitor	C1, C2, C3, C4, C5, C6, C7, C8, C9, C10, C11, C12	capc1608n	Cap	12
GRM188R72A104KA35 D	Capacitor	C13, C14, C15, C16, C17, C18, C19, C20, C21, C22, C23, C24	capc1608n	Cap	12
0.1uF	Capacitor	C25	capc1005n	Cap	1
LM94021BIMGX/NOP B		IC1	SC70-5	LM94021	1
M50-3130245R	Header, 2-Pin	J1, J2	m50-2	Header 2	2
CRCW060310M0FKEA C	Resistor	R1, R2, R3, R4, R5, R6, R7, R8, R9, R10, R11, R12, R13	resc1608n	Res2	13
1.5K	Resistor	R14	RESC1005N	Res2	1
10	Resistor	R15	RESC1005N	Res2	1
SiPMx8		SiPM1, SiPM2	SiPMx8	SiPMx8	2
AXT332124		U\$13, U\$14	AXT332124	AXT332124	2