

-> tester

Crate Configurations

- 1 : BU Test Stand
- 2 : D0 Test Stand
- 3 : North A-layer/MCON (MCCM 068)
- 4 : North B/C-Layer (MCCM 168)
- 5 : South A-layer/MCON (MCCM 088)
- 6 : South B/C-Layer (MCCM 188)
- 7 : Central MCON (MCCM 068)

Select crate to configure: 2

Initializing Constants

URNS = 0011

FCL2_DELAY = 0B01

T_CSR_1 = 7384

T_CSR_2 = 330005

STATUS_1 = AAF10004

PHYSICS_BOARD1 = pb_test_stand.dat

PHYSICS_BOARD2 = pb_test_stand.dat

1 MCEN's for testing

Address

F0820000 FDABFFFF

MCEN Test Stand Program

Available Functions:

- 1 : Init MCCM
- 2 : Program MCCM
- 4 : Init MCEN
- 5 : Program Physics Board
- 6 : Init MTT
- 7 : Empty L2 & L3 Waiting Lists
- 8 : Erase flash ROM
- 9 : Program flash ROM
- 10 : Loop Test
- 20 : Run in continuous mode
- 21 : Stop continuous mode

99 : Cold Start

-1 : Exit

Enter function number: 99

Detected uninitialized MCEN 2

MCCM initialized

MCEN F0820000 initialized

Programming Physics Board on MCEN F0820000... Done.

MTT_script_L1A[0] = 8

MTT_script_L2A[0] = 8

MTT_script_L1A[1] = 101

MTT_script_L2A[1] = 101

MTT initialized

Enter function number, 0 for menu: 10

Enter number of loops: 1

Enter number of turns per loop: 1

Enter number of events per turn: 2

Enter printout (1 = errors, 0 = none, -1 = raw data): -1

Error Rates

Turn	RX1	RX2	RX3	RX4	L2A	L2B	L2trig
Turn 0 Event 0							
FIFO 0a00 :	0108	aaaa 0208	aaaa 0308	aaaa			
FIFO 0a02 :	0408	aaaa 0508	aaaa 0608	aaaa			
FIFO 0a04 :	0708	aaaa 0808	aaaa 0908	aaaa			
FIFO 0a06 :	0a08	aaaa 0b08	aaaa 0c08	aaaa			

Reading L2 Data

L2A FIFO

a705 Hotlink Idle
a700 Hotlink Begin data

L2A Header

0006 # 16-bit words
0157 MCCM ID
0008 BC
0000 Turn
0002 Status Register 1
8000 Status Register 2

L2A Data

L2B FIFO

a705 Hotlink Idle
a700 Hotlink Begin data

L2B Header

0036 # 16-bit words
0157 MCCM ID
0008 BC
0000 Turn
0002 Status Register 1
8000 Status Register 2

L2B Data

Address	Data
0003 0108	
0007	aaaa
000b	0208
000f	aaaa
0013	0308
0017	aaaa
0083	0708
0087	aaaa
008b	0808
008f	aaaa
0093	0908
0097	aaaa
0043	0408
0047	aaaa
004b	0508
004f	aaaa
0053	0608
0057	aaaa
00c3	0a08
00c7	aaaa
00cb	0b08

00cf aaaa
00d3 0c08
00d7 aaaa

Reading L3 Data

a705
a700

L2trig Header L2Trig = L3

0000 Zero, as required by data format
004e # 32-bit words
009c # 16-bit words
0157 MCCM ID
0008 BC
0000 Turn
0002 Status Register 1
8000 Status Register 2

L2trig MCEN Header

0094 # 16-bit words
ff82 MCEN ID
0802 Status Register 1
f000 Status Register 2

L2trig MCEN Data

0408 0108 Centroid data
aaaa aaaa
0508 0208
aaaa aaaa
0608 0308
aaaa aaaa
0a08 0708
aaaa aaaa
0b08 0808
aaaa aaaa
0c08 0908
aaaa aaaa
0000 0108 Input data for RX 1 and 2
0000 aaaa
0208 0000
aaaa 0000
0000 0000
0000 0000
0408 0000 Input data for RX 3 and 4
aaaa 0000
0000 0000
0000 0000
0000 0308
0000 aaaa
0000 0000 Input data for RX 5 and 6
0000 0000
0000 0508
0000 aaaa
0608 0000
aaaa 0000
0000 0708 Input data for RX 7 and 8
0000 aaaa

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0808 0000
aaaa 0000
0000 0000
0000 0000
0a08 0000  Input data for RX 9 and 10
aaaa 0000
0000 0000
0000 0000
0000 0908
0000 aaaa
0000 0000  Input data for RX 11 and 12
0000 0000
0000 0b08
0000 aaaa
0c08 0000
aaaa 0000
0003 0108  L2 data
0007 aaaa
000b 0208
000f aaaa
0013 0308
0017 aaaa
0083 0708
0087 aaaa
008b 0808
008f aaaa
0093 0908
0097 aaaa
0043 0408
0047 aaaa
004b 0508
004f aaaa
0053 0608
0057 aaaa
00c3 0a08
00c7 aaaa
00cb 0b08
00cf aaaa
00d3 0c08
00d7 aaaa

```

Turn 0 Event 1

```

FIFO 0a00 : 0165 5555 0265 5555 0365 5555
FIFO 0a02 : 0465 5555 0565 5555 0665 5555
FIFO 0a04 : 0765 5555 0865 5555 0965 5555
FIFO 0a06 : 0a65 5555 0b65 5555 0c65 5555

```

Reading L2 Data

L2A FIFO

```

a708  Hotlink End data (from previous event)
a705
a700

```

L2A Header

```

0006
0157
0065

```

0000
0002
8000
L2A Data

L2B FIFO
a708
a705
a700

L2B Header
0036
0157
0065
0000
0002
8000

L2B Data
0003 0165
0007 5555
000b 0265
000f 5555
0013 0365
0017 5555
0083 0765
0087 5555
008b 0865
008f 5555
0093 0965
0097 5555
0043 0465
0047 5555
004b 0565
004f 5555
0053 0665
0057 5555
00c3 0a65
00c7 5555
00cb 0b65
00cf 5555
00d3 0c65
00d7 5555

Reading L3 Data
a708
a705
a700

L2trig Header
0000
004e
009c
0157
0065
0000
0002

8000

L2trig MCEN Header

0094

ff82

6502

f080

L2trig MCEN Data

0465 0165

5555 5555

0565 0265

5555 5555

0665 0365

5555 5555

0a65 0765

5555 5555

0b65 0865

5555 5555

0c65 0965

5555 5555

0000 0165

0000 5555

0265 0000

5555 0000

0000 0000

0000 0000

0000 0000

0465 0000

5555 0000

0000 0000

0000 0000

0000 0365

0000 5555

0000 0000

0000 0000

0000 0565

0000 5555

0665 0000

5555 0000

0000 0765

0000 5555

0865 0000

5555 0000

0000 0000

0000 0000

0a65 0000

5555 0000

0000 0000

0000 0000

0000 0965

0000 5555

0000 0000

0000 0000

0000 0b65

0000 5555

0c65 0000

5555 0000

0003 0165
0007 5555
000b 0265
000f 5555
0013 0365
0017 5555
0083 0765
0087 5555
008b 0865
008f 5555
0093 0965
0097 5555
0043 0465
0047 5555
004b 0565
004f 5555
0053 0665
0057 5555
00c3 0a65
00c7 5555
00cb 0b65
00cf 5555
00d3 0c65
00d7 5555

2 events took 4.72e-03 hours

LEVEL 1 ERRORS

RX #1 - 0 Errors, Error rate = 0.00e+00
RX #2 - 0 Errors, Error rate = 0.00e+00
RX #3 - 0 Errors, Error rate = 0.00e+00
RX #4 - 0 Errors, Error rate = 0.00e+00

LEVEL 2 ERRORS

0 bad L2 events

L2A - 0 Errors, Error rate = 0.00e+00
L2B - 0 Errors, Error rate = 0.00e+00

LEVEL 3 ERRORS

L2trig - 0 Errors, Error rate = 0.00e+00