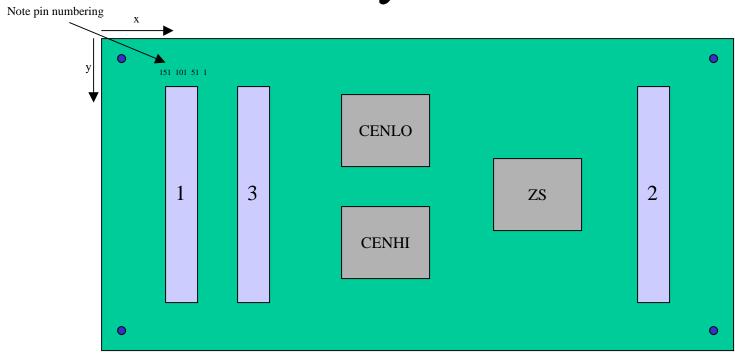
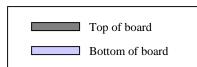
New Physics Board



Width: 9.400" Height: 3.450"



Connector 1:

X: 1.0" Y: 0.5"

Connector 2

X: 8.5" Y: 0.5"

Connector 3

X: 2.0" Y: 0.5"

(This is the coordinate of the baseline pin, which in this case is number **51**) Mounting holes:

1) X: 0.25" Y: 0.25"

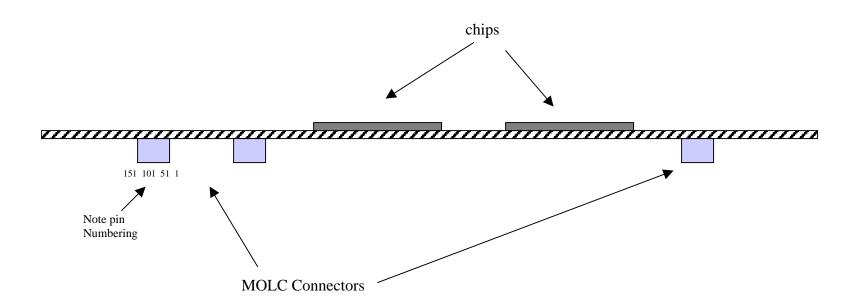
2) X: 9.150" Y: 0.25"

3) X: 0.25" Y: 3.200"

4) X: 9.150" Y: 3.200"

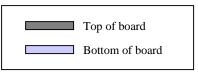
Holes are 110 mil diameter, 130 mil pad, and 150 mil power anti-pad. Use padstacks in ALL cases

Components in Board

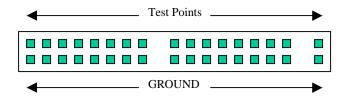


Other Notes:

- * Voltage Regulator for 2.5 as close enough from the 3.3V source as possible
- * Use bigger vias for power nets, except under BGA chips



Test Points

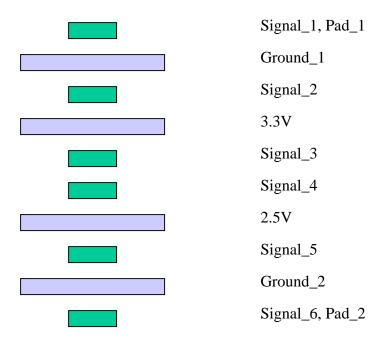


Test Point Sequence (left to right): TP1 TP2 TP3 TP4 TP5 TP6 **TP7** TP8 TP9 **TP10** TP11 **TP12 TP13 TP14 TP15 TP16** CLK

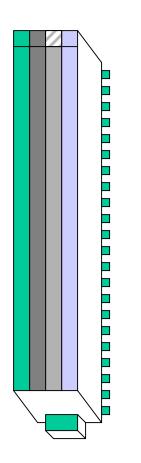
Please join test points as follows: Use <u>0.1" spacing straight headers</u>. Separation between blocks is <u>0.2"</u>

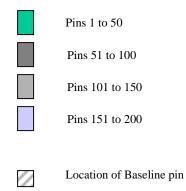
<u>Test Points TP17, 2.5V and 3.3V</u> <u>should be single pins of the same</u> <u>type of headers</u>

Recommended Stack-Up



Connector Pin Numbering





Note: This is looking at the bottom part of the board. So, in fact, the baseline pin is the closest thru-hole pin to the origin.