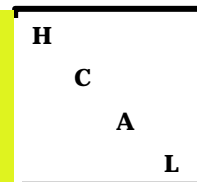




HCAL RBX PRR

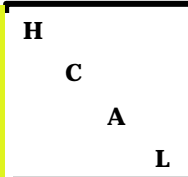


Readout Box (RBX) PRR

Dan Green
HCAL Project Manager



Outline

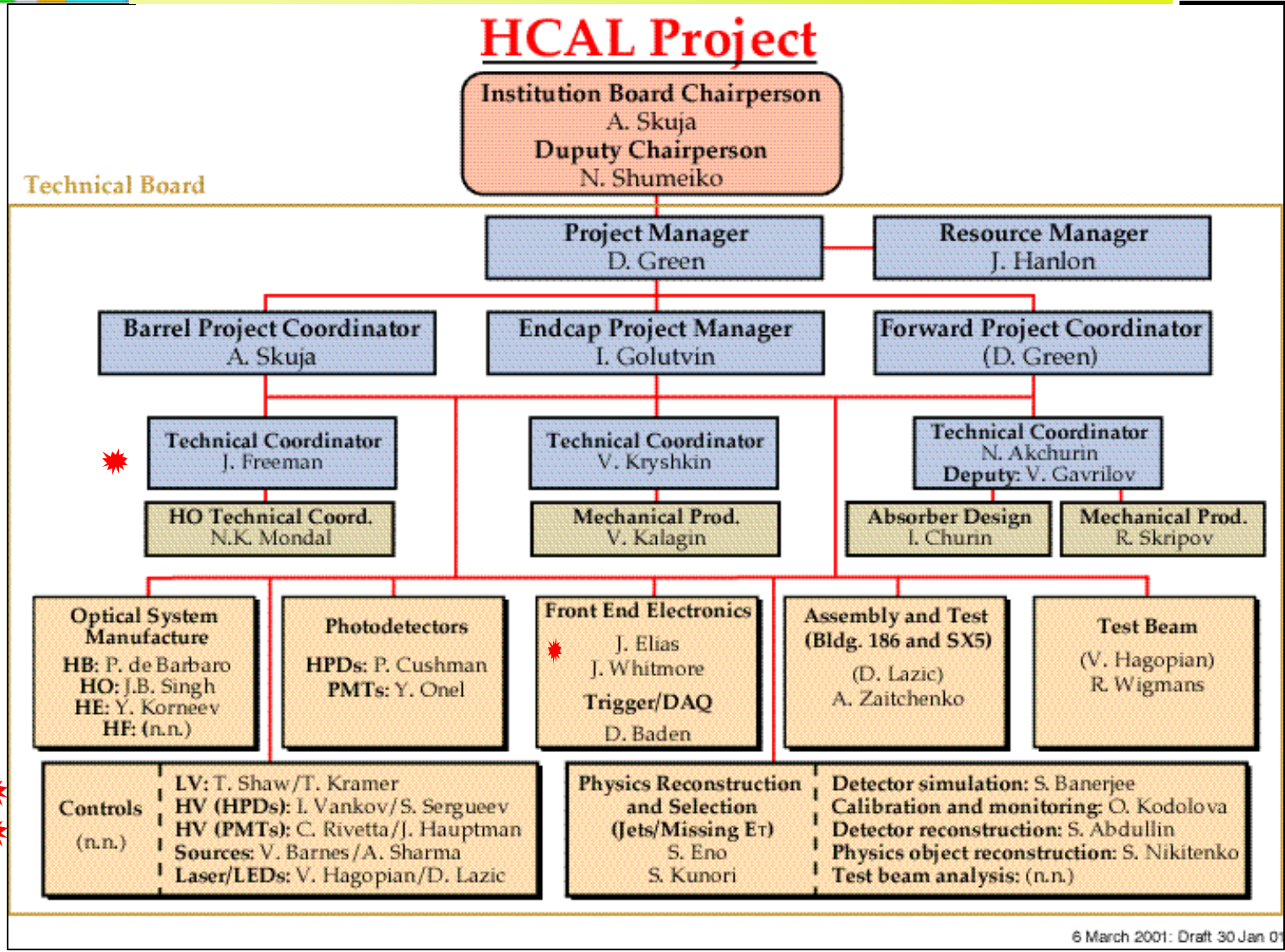


- **Schedule and Milestones**
- **R&D experience**
- **RBX components**
 - **shell**
 - **backplanes**
 - **ODU**
 - **HPD**
 - **Calib - laser, LED, sources**
 - **Services - HV, LV, cooling,**
 - **Controls**



HCAL Organization

H
C
A
L

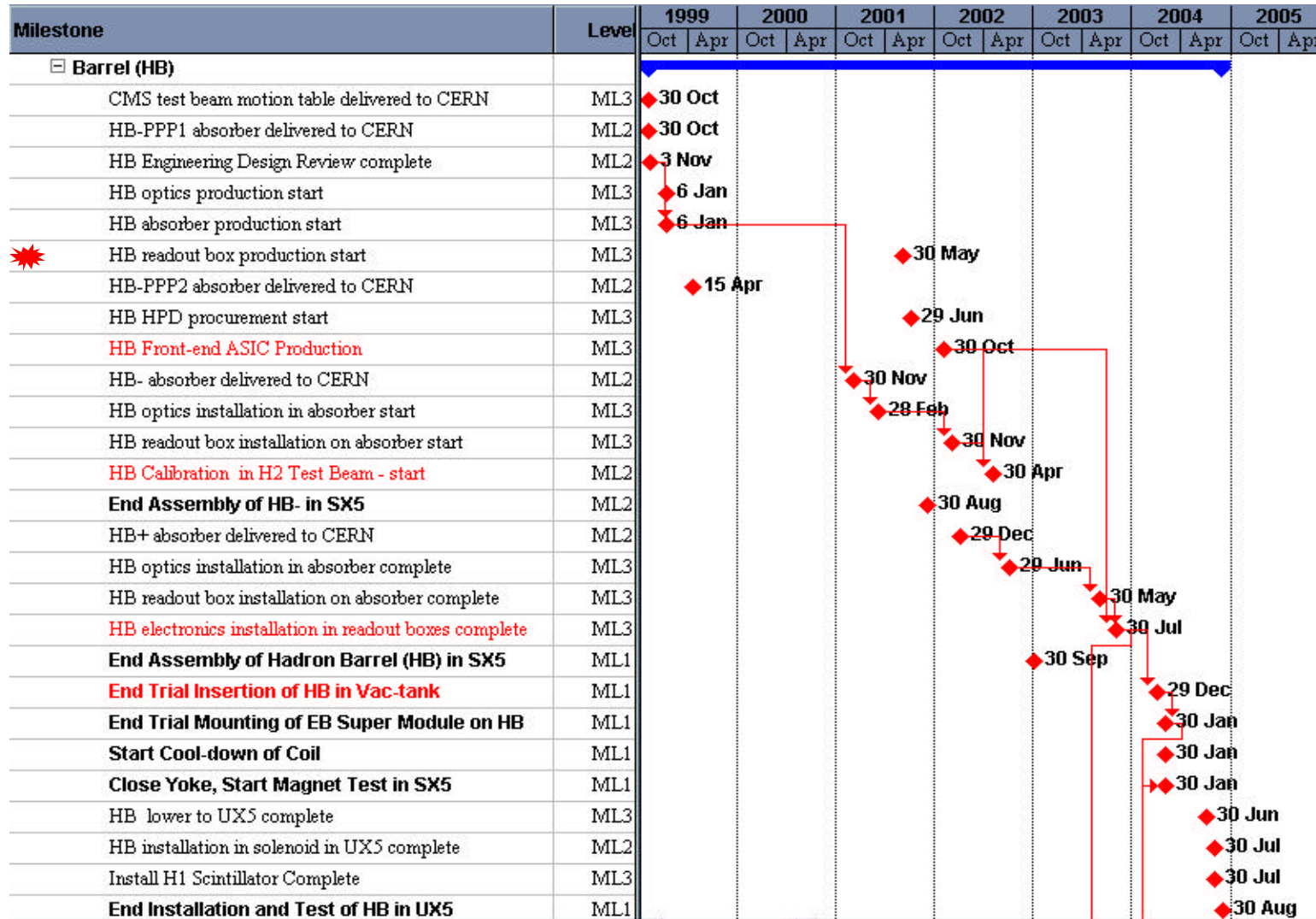


6 March 2001: Draft 30 Jan 01



HCAL - ML, HB

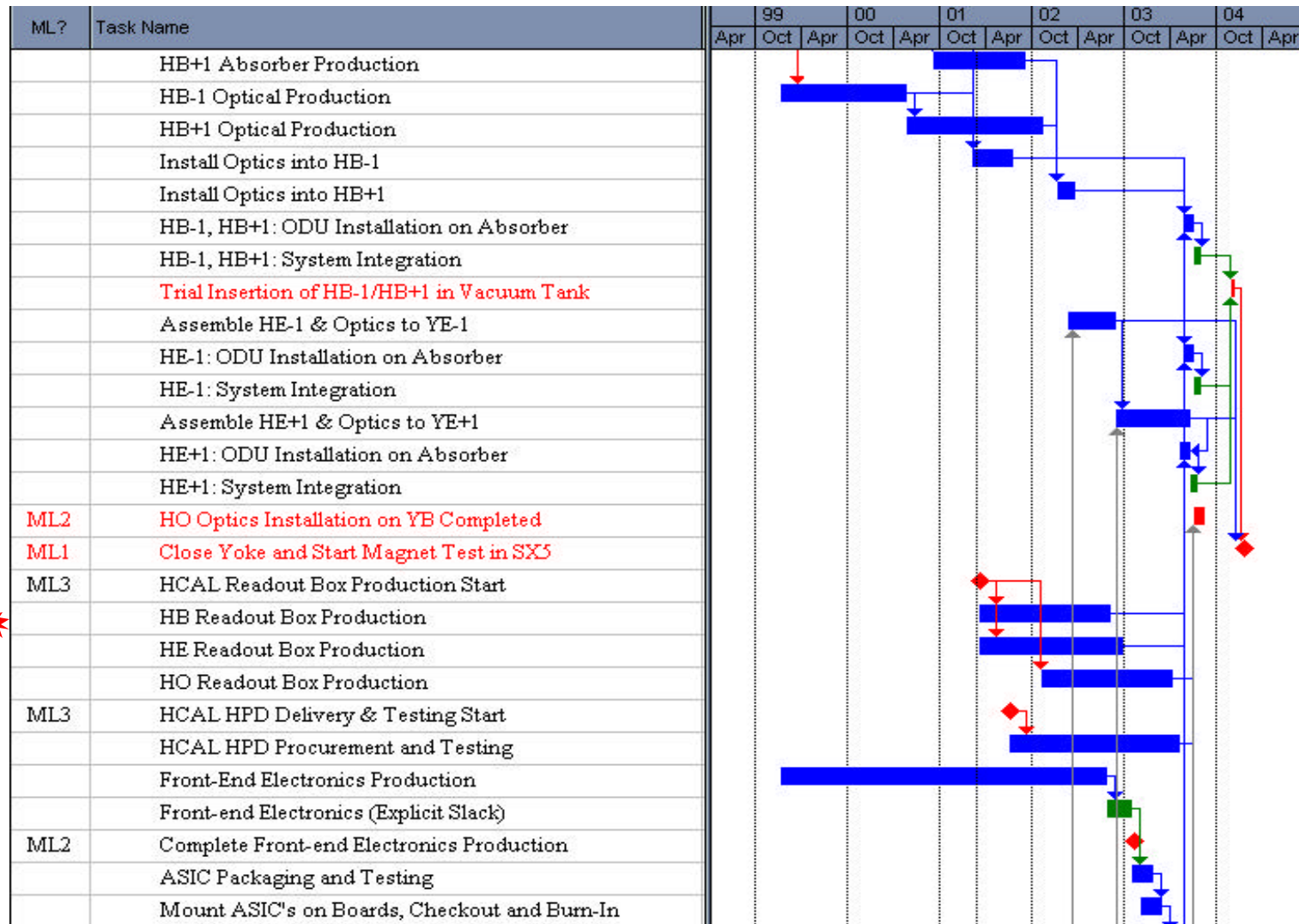
H
C
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L





HCAL Critical Path and RBX

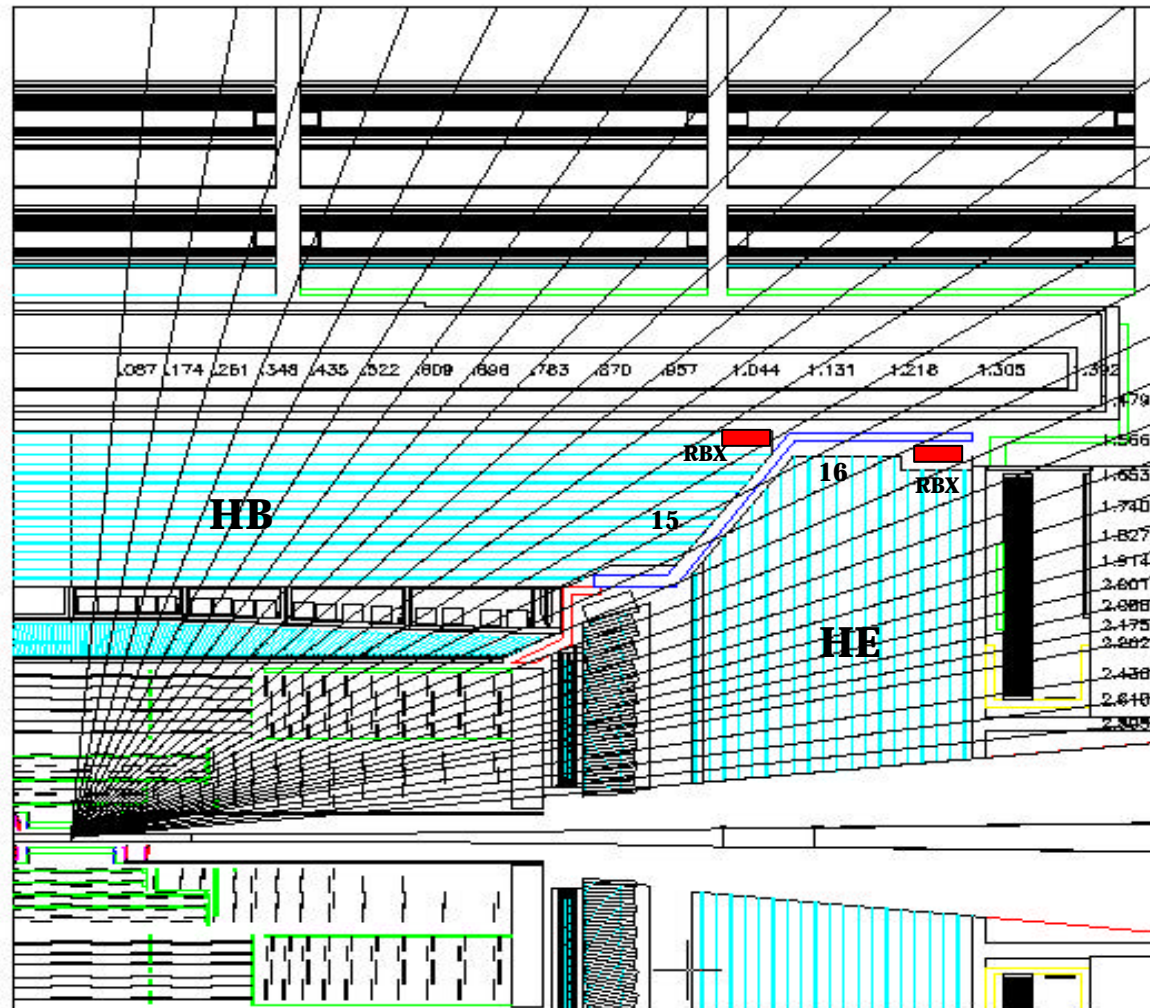
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Central HCAL

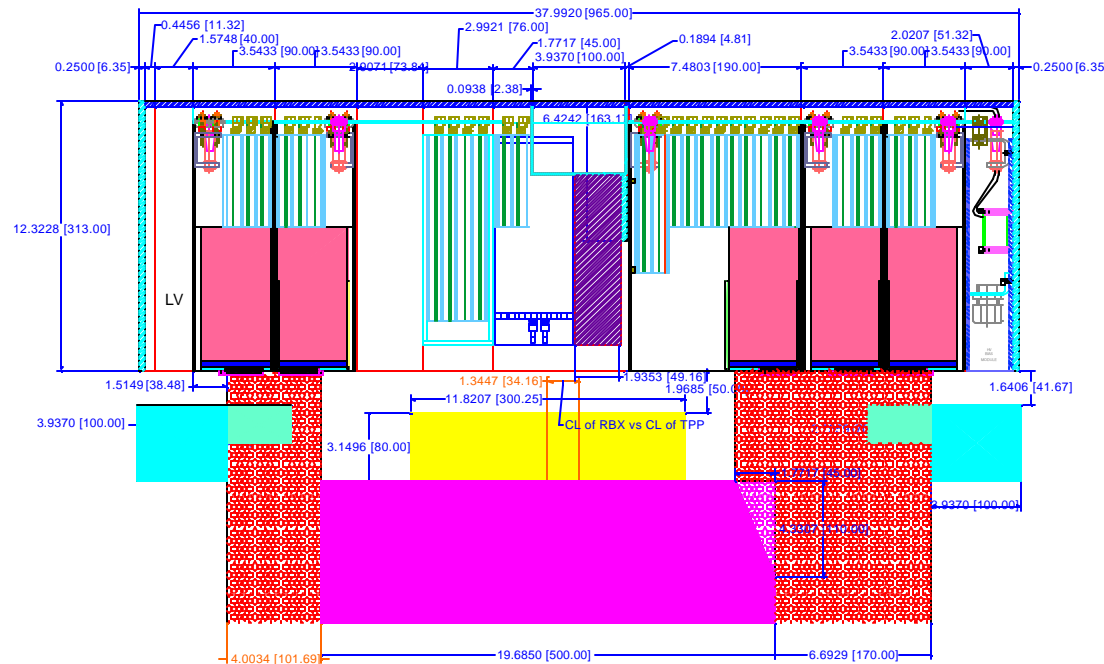
H
C
A
L





HB RBX Detail View

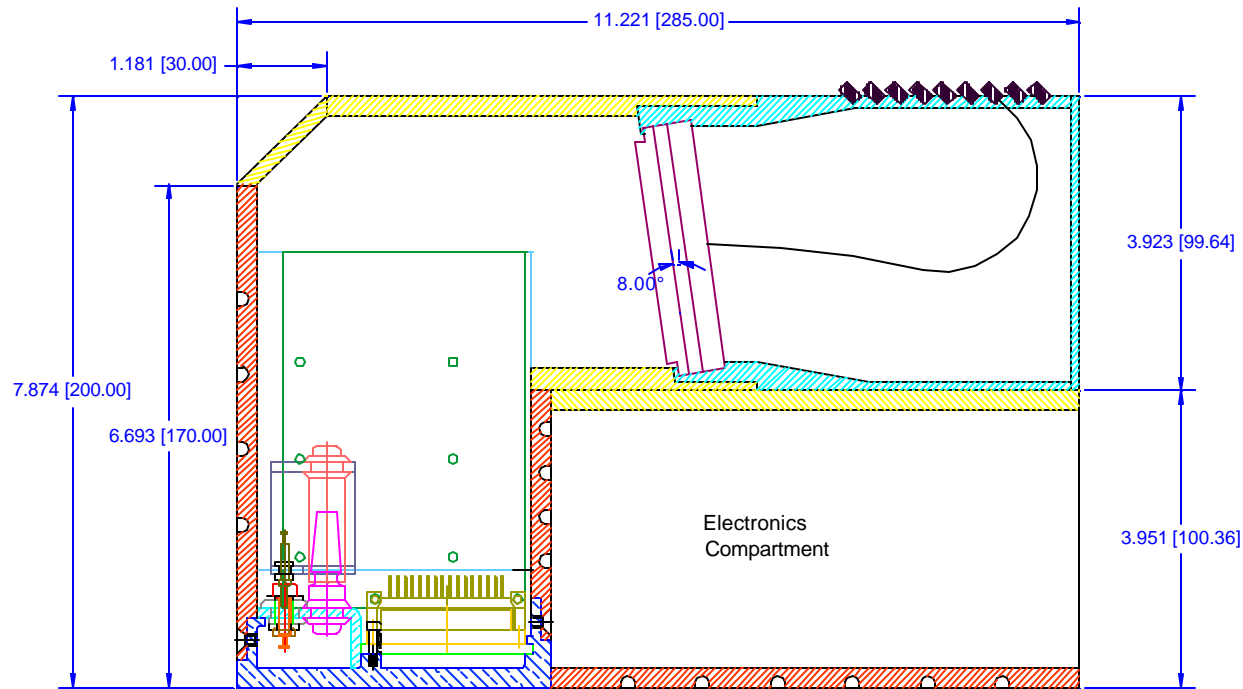
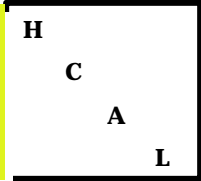
H
C
A
L



The issue for the RBX is the access to the HPD. There are integration issues with the tracker patch panel. A mockup was made and changes appear possible which will ease the problem. Schedule is such that the RBX is near to the critical path.



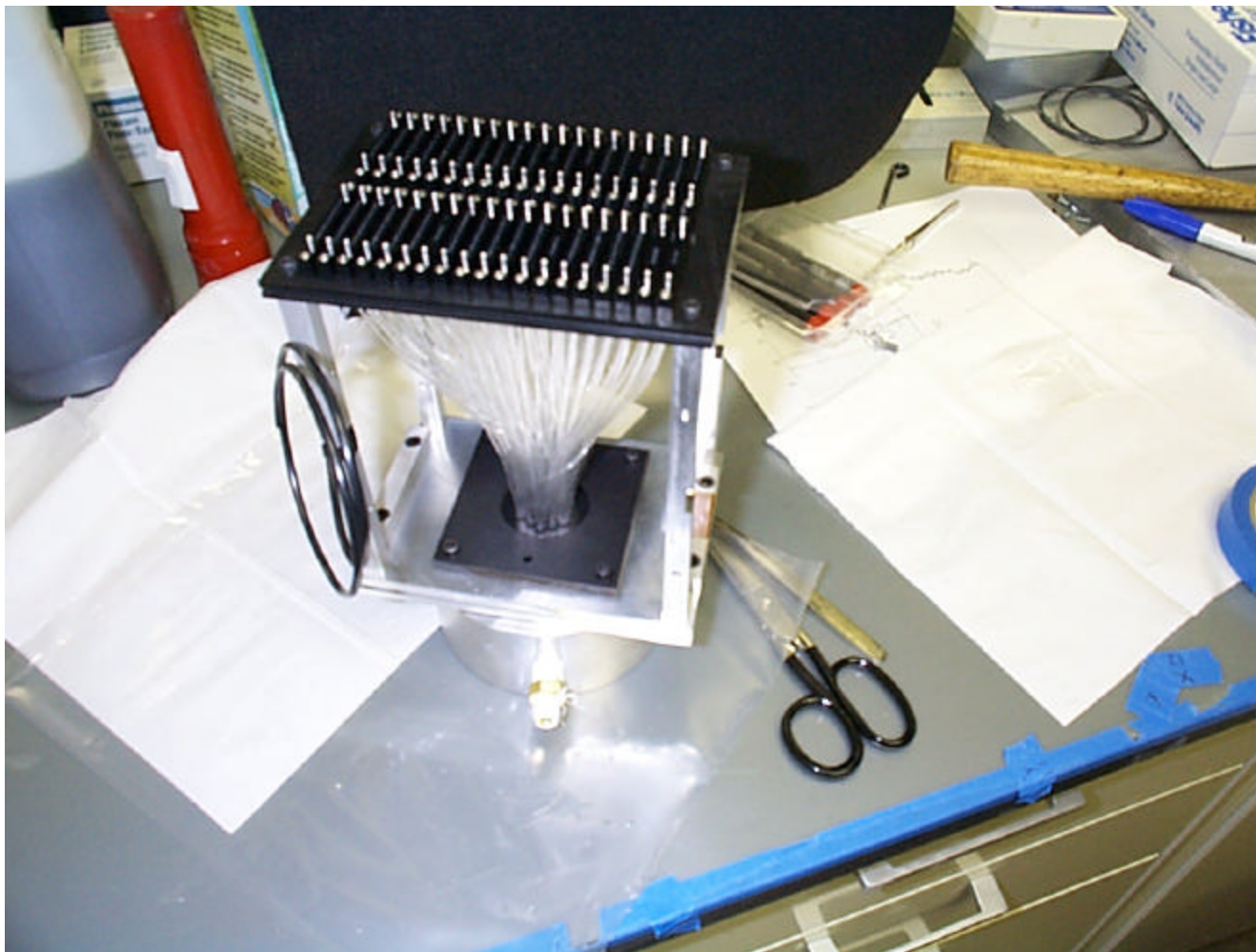
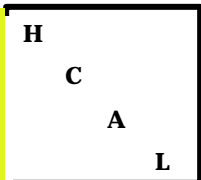
HE RBX Conceptual Design



HE can go ahead with the RBX, it appears, as the overlap of services and cables with other subsystems is not as severe as it is with HB.

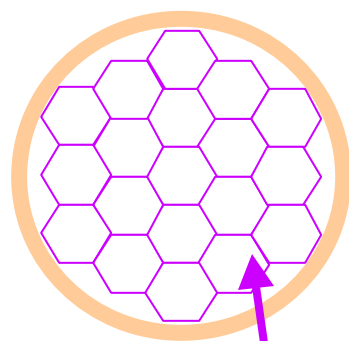
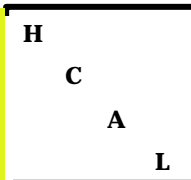


ODU - Optics for Layer-->Tower

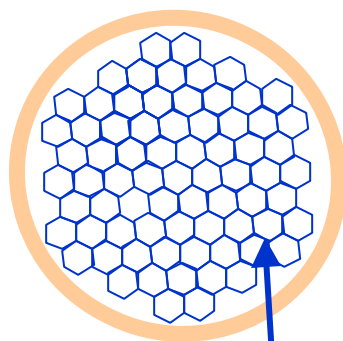




HPD



19 x 5.5mm

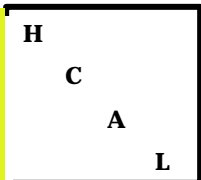


73 x 2.75mm

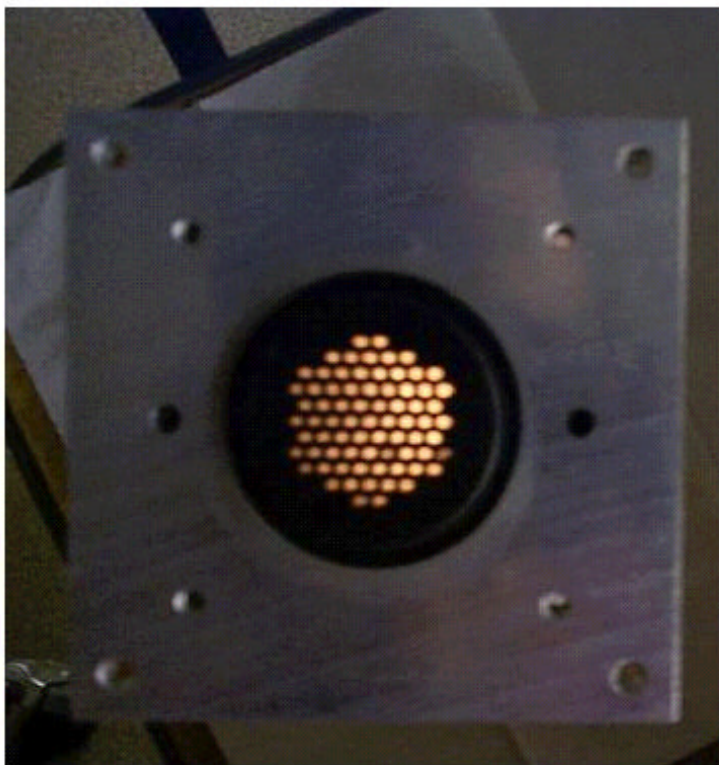
The HPD have had many problems which have been solved - sequentially. At present we specify an Al electrode and an anti-reflection coating. This “works” but the problem of low yields at the vendor must be solved.



H1 and H2 Layers



73-channel (HB)

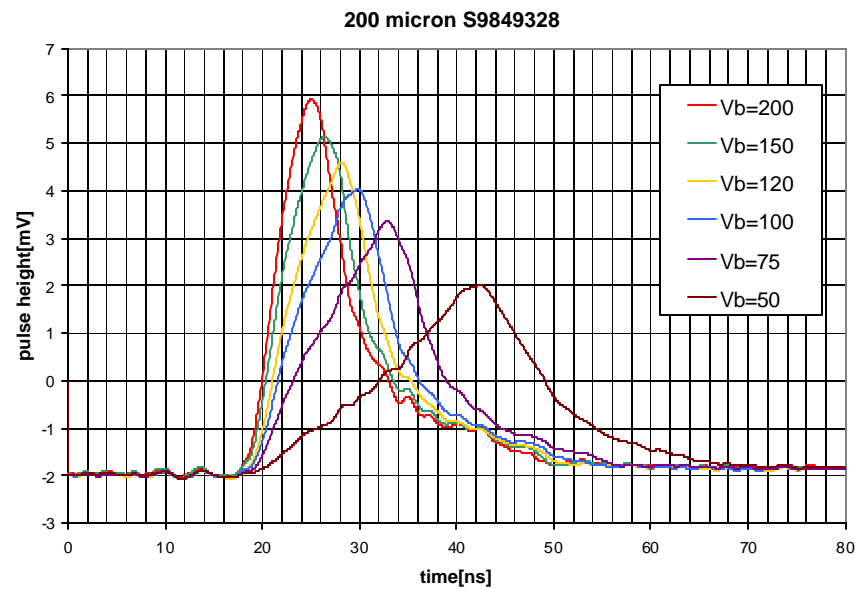
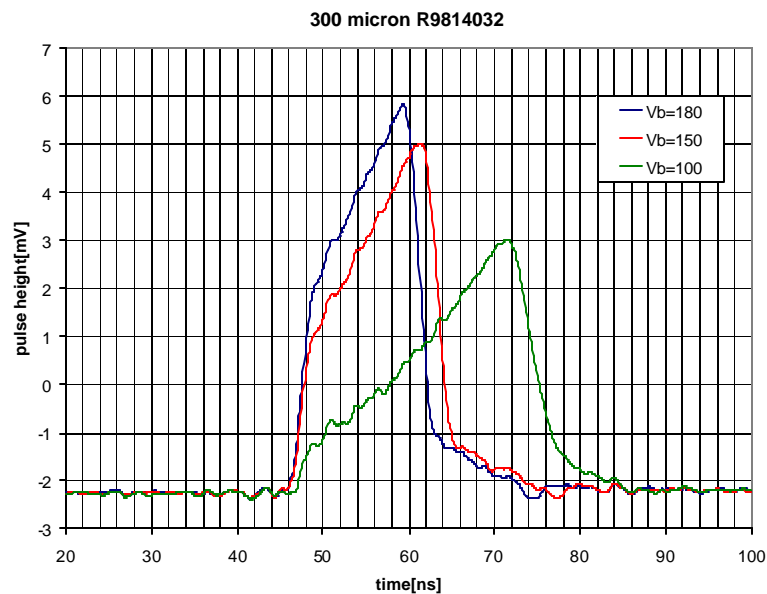
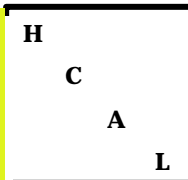


19-channel (HB right)





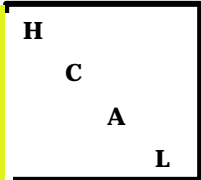
Pulse Formation - Bias



We have developed at fast diode which - with guard rings - can be run at very high bias.



Near Term HPD R&D

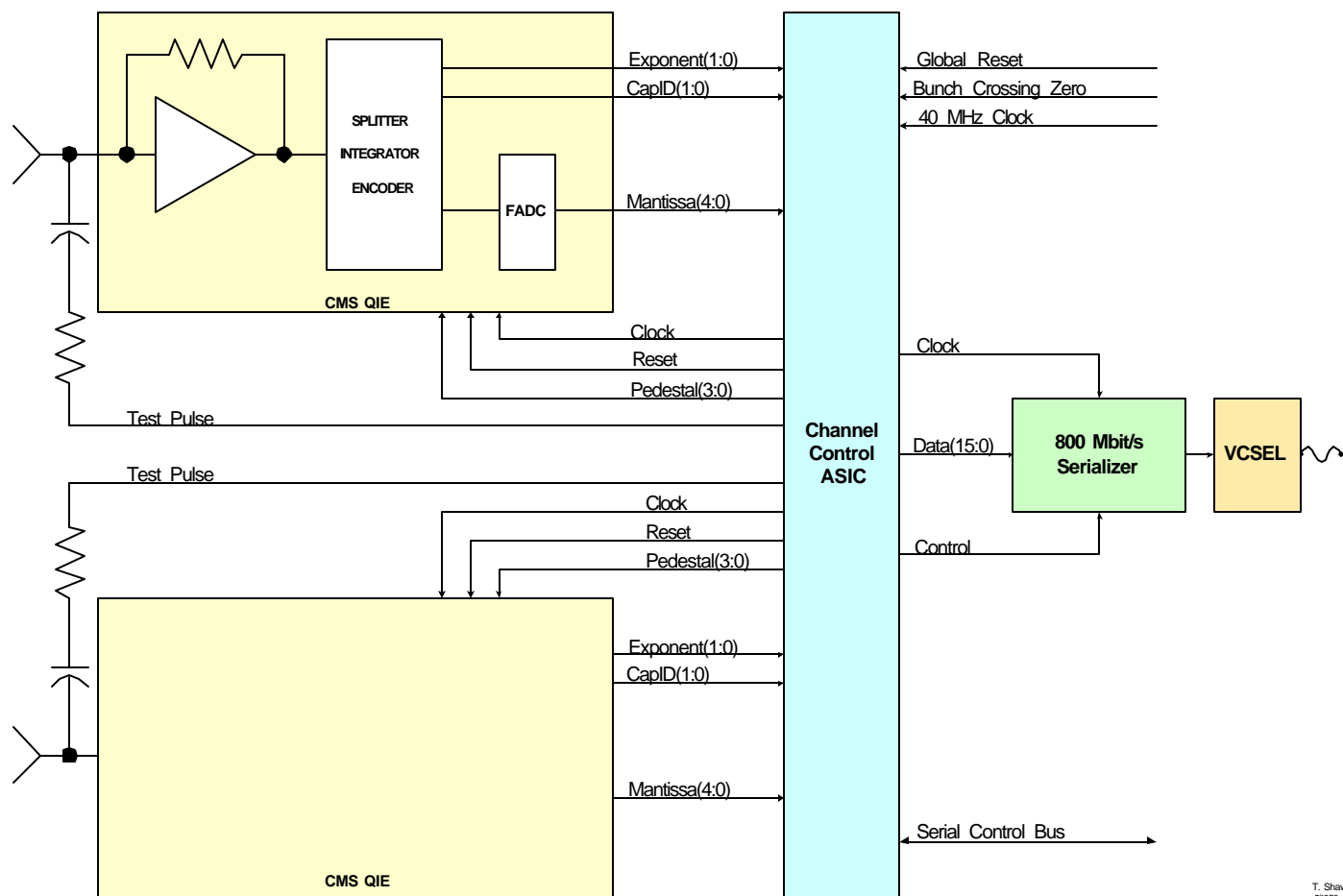
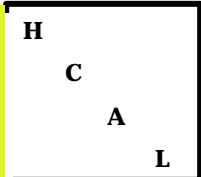


DEP=red
 Canberra=yellow
 MN=green

	M	A	M	J	J	A	S	O	N
production diode order + 2 wafers	Yellow	Yellow							
Type 1 Development									
Canberra process developm.		Yellow	Yellow	Yellow					
DEP 16 nm a-si on Al test	Red	Red							
MN reflexion measurement		Green							
2 HP D a-Si/Al prototypes			Red	Red	Red				
evaluation by MN HPD prototypes					Green	Green			
finish 2 wafers option 1				Yellow	Yellow				
evaluate leakage current after bake out					Red	Red			
finish first production run wafers						Yellow	Yellow		
Type 2 Development									
SiO2/Ag/a-si coating on rejected diodes	Red	Red	Red						
MN reflexion & leakage studies			Green	Green					
finish 2 wafers option 2			Yellow	Yellow					
DEP SiO2/Ag/a-Si 3 HPD prototypes				Red	Red	Red			
evaluation by MN HPD prototypes						Green	Green		
Production									
DEP Production start							Red	Red	Red
First Production HPDs at MN									Green



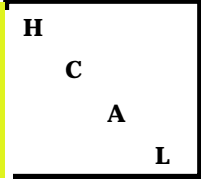
HCAL QIE-based Front End



CMS QIE Solution



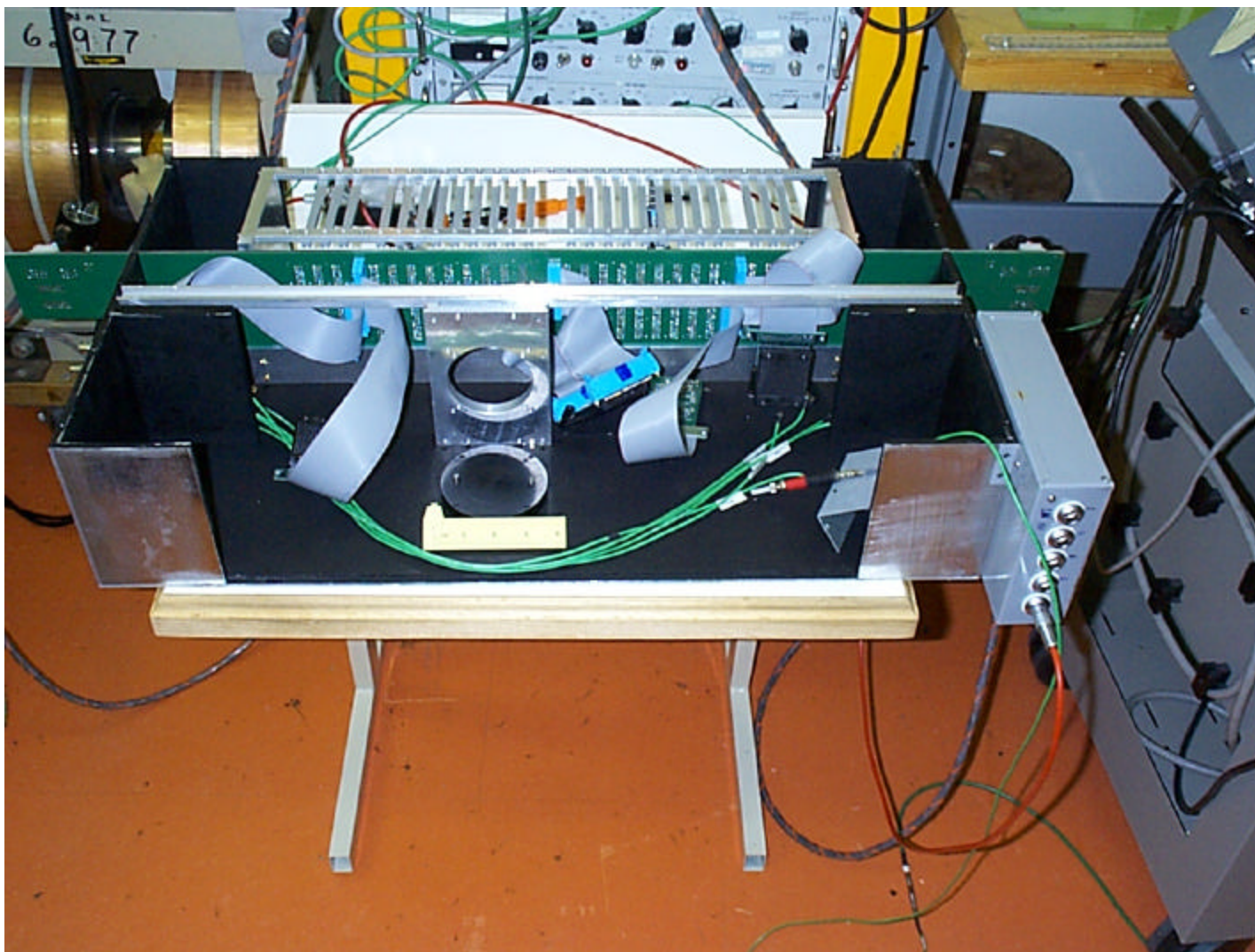
QIE Schedule





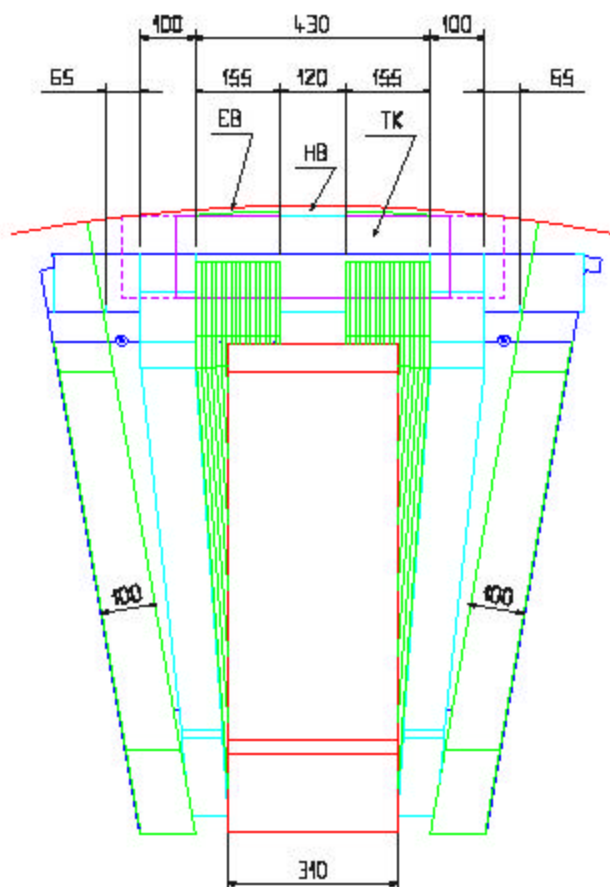
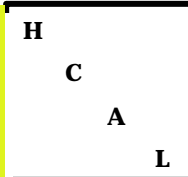
Decoder Box, Test Beam R&D

H
C
A
L





Access to FE Electronics



53 degree end of HB wedge.

Blue = HCAL services

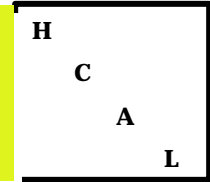
Green = ECAL

Red = Tracker

End of HB wedges obscured by cables and services from HB, EB, tracker. Servicing electronics nontrivial.



Goals



- **We have a ~ final design which has been on hold for 6 months.**
- **The schedule for RBX is near to the critical path.**
- **The design has been sent to CMS for evaluation and optimization.**
- **We need approval of the RBX - optical cables, ODU, shell, etc. The HPD and QIE are not yet ready for prime time.**